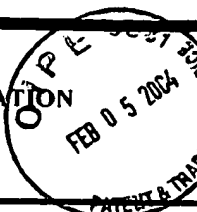


INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

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Applicant(s)

asuji Norito

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
tl		6,198,239 B1	03/06/2001	Blackburn			
tl		6,304,088 B1	10/16/2001	Yee			
tl		3,642,329	02/15/1972	Zechall et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
tl		10-197572	07/31/1998	Japan			(AES only)	
		FR 2 817 970	06/14/2002	France				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		European Search Report dated January 14, 2004.
		"Voltage Comparator Circuit", IBM Technical Disclosure Bulletin, Vol. 17, No. 4, September 1974, pp. 1151-1152, XP-002265879.

EXAMINER

tl

DATE CONSIDERED

12-10-04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.